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Fig. 1

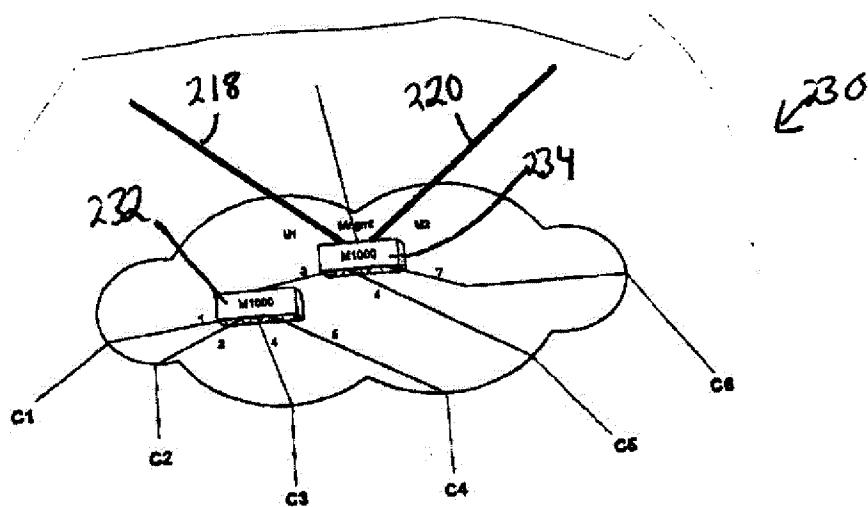


Fig. 2

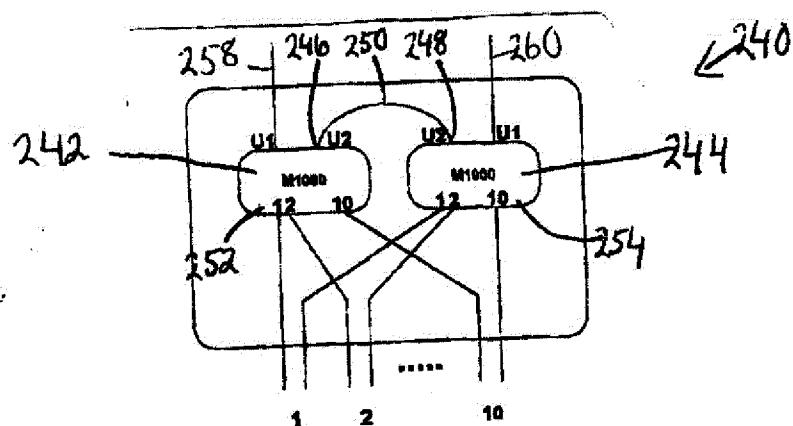
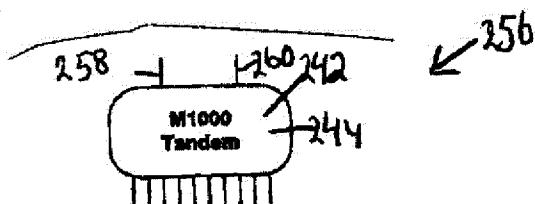


Fig. 3



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Fig.4

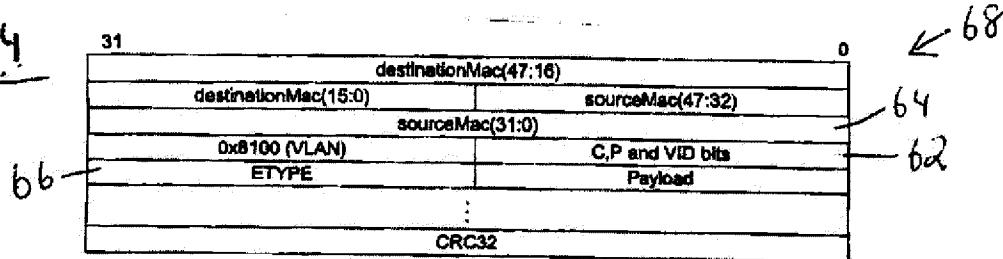


Fig.5

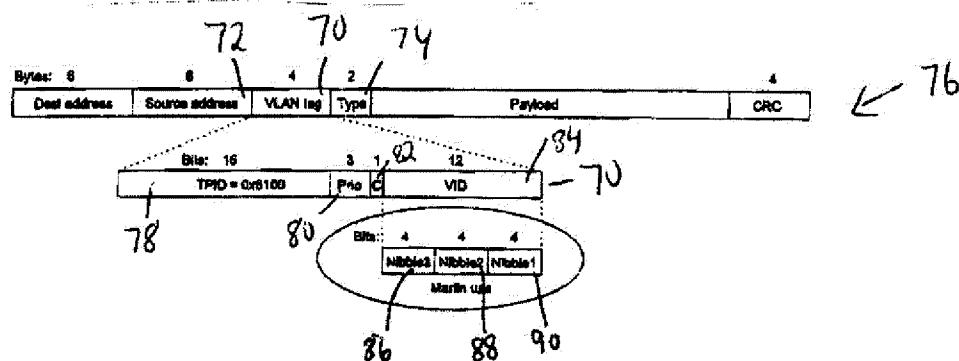


Fig.6

Address	Description
0x0	This level not addressed
0x1-0xA	Address for ports 1-10
0xB-0xD	Reserved
0xE	Address for AUX port
0xF	Address for ring topology detection
Untagged	Address for OAM in unit

↙ 92

Fig.7

Nibble	Valid values
Nibble 3	0x0-0xA, non zero only if Nibble 2 is non zero
Nibble 2	0x0-0xA
Nibble 1	0x1-0xA, 0xE, 0xF

↙ 92

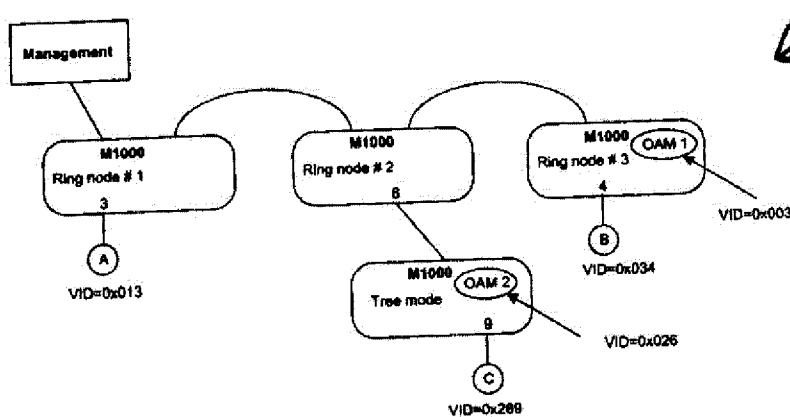
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Fig. 8

Address	First nibble	Second nibble
0x0	This level not addressed	This level not addressed
0x1-0xA	Ring node 1-10	Port 1-10
0xB-0xC	Ring node 11-12	Reserved
0xD	Reserved	Reserved
0xE	Reserved	Address for AUX port
0xF	Reserved	Address for topology detection
Untagged	Reserved	Address for OAM in unit

95

Fig. 9



97

Fig. 10

Nibble	Valid values
Nibble 3	0x0-0xC, non zero only if Nibble 2 is non zero, 0xB or 0xC only if this field is ring node number
Nibble 2	0x0-0xC, 0xB or 0xC only if this field is ring node number
Nibble 1	0x1-0xA, 0xE, 0xF

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133, 134, 135

Fig. 11

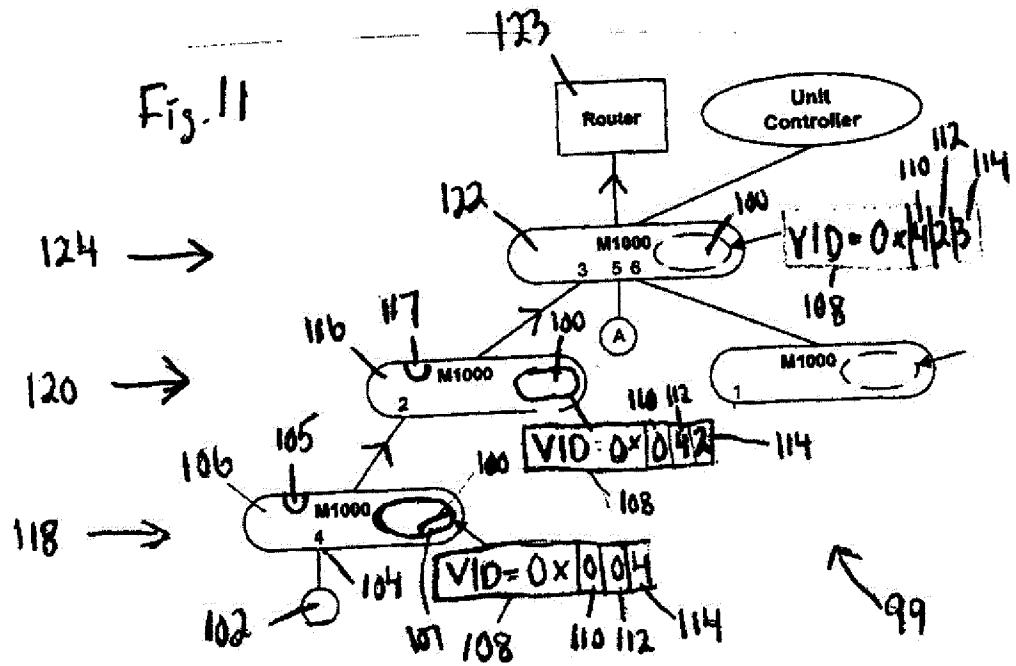


Fig. 12

